Dual-band wireless LANs(802.11 a/b/g/n/ac) and Diversity antenna switching



Features

Control voltage :

$$VC(H) = 1.8 \text{ to } 5.0 \text{ V } (3.0 \text{V TYP.})$$

 $VC(L) = -0.2 \text{ to } 0.2 \text{ V } (0 \text{V TYP.})$

Low insertion loss:

High isolation :

• Handling power:

$$P_{in(1dB)} = +38 \text{ dBm TYP.} @ f = 6.0 \text{ GHz},$$

VC(H) = 3.0 V, VC(L) = 0 V

Applications

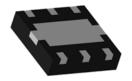
Dual-band wireless LANs
 (802.11 a/b/g/n/ac) and Diversity antenna swiching

Package

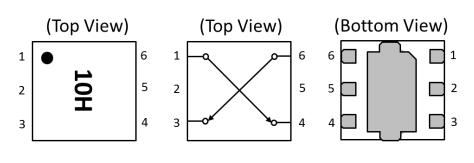
6-pin Thin SON Package (XS03) (1.5mm x 1.5mm x 0.37mm)

Description

 The CKRF6381XS03 is a pHEMT GaAs MMIC high power DPDT switch which was developed for Dual-band wireless LANs and Diversity antenna switching.



Pin Configuration and Internal Block Diagram



Pin No.	Pin Name		
1	ANT2		
2	VC2		
3	RX		
4	TX		
5	VC1		
6	ANT1		

Remark Exposed pad: GND

Ordering Information

Part Number	Order Number	Package	Marking	Supplying Form	
CKRF6381XS03-C2	CKRF6381XS03-C2	6-pin TSON	10H	•Embossed tape 8 mm wide	
		(Pb-Free)		•Pin 1, 6 face the perforation	
				side of the tape	
				·Qty 10 kpcs/reel	

Dual-band wireless LANs(802.11 a/b/g/n/ac) and Diversity antenna switching



Absolute Maximum Ratings

 $(TA = +25^{\circ}C, unless otherwise specified)$

Parameter	Symbol	Rating	Unit
Control Voltage	VC	6.0 ^{Note 1}	V
Input Power	Pin	+38.0 ^{Note 2}	dBm
Operating Ambient Temperature	T _A	-45~+85	${\mathfrak C}$
Storage Temperature	T_{stg}	-55~+150	$^{\circ}$

Note

- 1. |VC1 VC2|≤6.0V
- 2. 3.0V≦|VC1 VC2|≦5.0V

Recommended Operating Range

 $(T_A = +25$ [°]C, unless otherwise specified)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Operating Frequency	f	0.1	-	6.0	GHz
Switch Control Voltage (H)	VC(H)	+1.8	+3.0	+5.0	V
Switch Control Voltage (L)	VC(L)	-0.2	0	+0.2	V

Truth Table

VC1	VC2	ANT1 to TX	ANT1 to RX	ANT2 to TX	ANT2 to RX	
High	Low	OFF	ON	ON	OFF	
Low	High	ON	OFF	OFF	ON	

CDS-0030-02 Page 2 of 6





· Electrical Characteristics

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss	Lins1	f = 0.1 to 1.0 GHz	-	0.20	0.40	dB
	Lins2	f = 1.0 to 2.5 GHz	-	0.50	0.70	dB
	Lins3	f = 2.5 to 4.9 GHz	-	0.70	0.95	dB
	Lins4	f = 4.9 to 6.0 GHz		0.80	1.10	dB
Isolation	ISL1	f = 0.1 to 1.0 GHz	28	31	-	dB
	ISL2	f = 1.0 to 2.5 GHz	19	23	-	dB
	ISL3	f = 2.5 to 4.9 GHz	13	17	-	dB
	ISL4	f = 4.9 to 6.0 GHz	10	14	-	dB
Return Loss 1	RL1	f = 0.1 to 4.0 GHz	15	20	-	dB
Return Loss 2	RL2	f = 4.0 to 6.0 GHz	10	15	-	dB
0.1 dB Loss Compression	P _{in(0.1 dB)}	f = 0.1 to 6.0 GHz	_	+35	-	dBm
Input Power Note1	I in(0.1 dB)	1 dB)		+33		аып
1 dB Loss Compression	P _{in(1 dB)}	f = 0.1 to 6.0 GHz	_	+38	_	dBm
Input Power Note2	In(1 dB)			130		abiii
3rd Order Input Intercept		Pin = +20dBm,		+62	-	dBm
Point	IIP3	f = 0.1 to 6.0 GHz,	-			
FOILE		2-tone 1MHz Spacing				
2nd Harmonic	2f0	Pin = +25dBm,	_	-85	-	dBc
2nd Harmonic		f = 0.1 to 6.0 GHz	_			
3rd Harmonic	3f0	Pin = +25dBm,		-85	-	dBc
	310	f = 0.1 to 6.0 GHz	-			
Switch Control Current	\mathbf{I}_{cont}	No RF Signal	-	16	-	uA
Switch Control Speed	T_sw	50% CTL to 90/10% RF	-	100		ns

Note $P_{in(1dB)}$ is the measured input power level when the insertion loss increases 1dB more than that of the linear range.

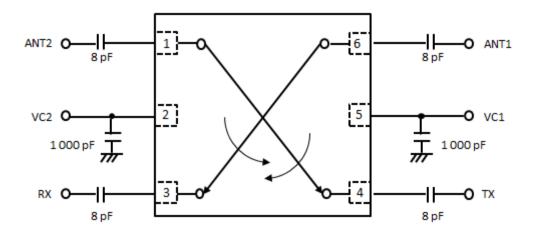
CDS-0030-02

Date Published Aug 2016

Dual-band wireless LANs(802.11 a/b/g/n/ac) and Diversity antenna switching



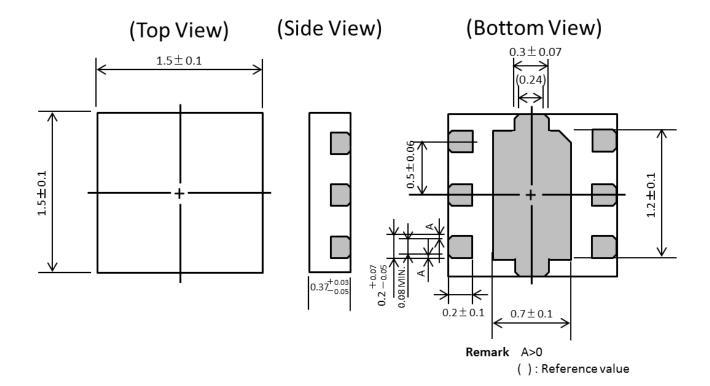
Evaluation Circuit



The application circuits and their parameters are for reference only and are not intended for use in actual design-ins. This device is used it is necessary to use DC Block Capacitance.

Package Dimensions

6-pin Thin SON Package (XS03) (Unit: mm)



CDS-0030-02 Date Published Aug 2016

Dual-band wireless LANs(802.11 a/b/g/n/ac) and Diversity antenna switching



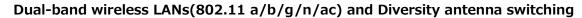
[CAUTION]

- All information included in this document is current as of the date this document is issued. Such information, however, is subject to change without any prior notice.
- You should not alter, modify, copy, or otherwise misappropriate any CDK product, whether in whole or in part.
- CDK does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of CDK products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of CDK or others.
- Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. CDK assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
- CDK has used reasonable care in preparing the information included in this document, but CDK does not warrant that such information is error free. CDK assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.
- Although CDK endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions.
 - Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a CDK product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures

 Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.
- Please use CDK products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive.
 CDK assumes no liability for damages or losses occurring as a result of your noncompliance with applicable
- This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written consent of CDK.

CDS-0030-02 Page 5 of 6

laws and regulations.





[Caution in the gallium arsenide (GaAs) product handling]

This product uses gallium arsenide (GaAs) of the toxic substance appointed in laws and ordinances. GaAs vapor and powder are hazardous to human health if inhaled or ingested.

- Do not dispose in fire or break up this product.
- Do not chemically make gas or powder with this product.
- When discard this product, please obey the law of your country.
- Do not lick the product or in any way allow it to enter the mouth.

[CAUTION]

Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

CHUO DENSHI KOGYO CO., LTD

3400 Kooyama, Matsubase, Uki-City,

Kumamoto 869-0512, Japan Tel: +81-964-32-2730 Fax: +81-964-32-3549

URL: http://www.en.cdk.co.jp/

Contact info for inquiries

Electronic Devices Division Sales and Planning Department

TEL : +81-964-32-2750 E-mail : info@cdk.co.jp FAX : +81-964-32-3549

CDS-0030-02 Page 6 of 6