### 12GHz Super Low Noise FET in Hollow Plastic PKG



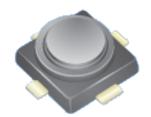
#### Features:

Super Low noise figure and high associated gain

NF=0.30dB TYP., Ga=13.7dB TYP. @VDS=2V, ID=10mA, f=12GHz

#### **Description:**

- Super Low Noise and High Gain
- Hollow (Air cavity) Plastic package



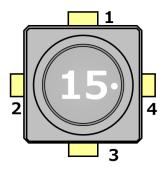
## **Applications:**

Ku-band LNB(Low Noise Block)
 \*Suitable for 1<sup>st</sup> stage

#### Package:

Micro-X plastic package

## **PIN Configuration:**



| PIN No. | PIN Name |  |  |
|---------|----------|--|--|
| 1       | Source   |  |  |
| 2       | Drain    |  |  |
| 3       | Source   |  |  |
| 4       | Gate     |  |  |

#### **Ordering Information:**

| Part Number       | Order Number      | Package         | Marking | Supplying Form               |
|-------------------|-------------------|-----------------|---------|------------------------------|
| CKRF7512CK24-C1-J | CKRF7512CK24-C1-J | Micro-X plastic | 15      | •Embossed 8 mm wide          |
|                   |                   | package         |         | •Pin 4 (Gate) faces the      |
|                   |                   |                 |         | perforation side of the tape |
|                   |                   |                 |         | ·Qty 10Kpce/reel             |

### 12GHz Super Low Noise FET in Hollow Plastic PKG



## **Absolute Maximum Ratings:**

(TA=+25℃, unless otherwise specified)

| Parameter               | Symbol | Rating         | Unit          |
|-------------------------|--------|----------------|---------------|
| Drain to Source Voltage | VDS    | 4.0            | V             |
| Gate to Source Voltage  | VGS    | -3.0           | V             |
| Drain Current           | ID     | IDSS           | mA            |
| Gate Current            | IG     | 80             | μΑ            |
| Total Power Dissipation | Ptot   | 125            | mW            |
| Channel Temperature     | Tch    | +150           | ℃             |
| Storage Temperature     | Tstg   | -55 to +125    | ℃             |
| Operation temperature   | Тор    | -55 to +125 *1 | ${\mathbb C}$ |

<sup>\*1 :</sup> Relationship of Ambient Temperature and Total Power Dissipation, please refer to the Page 3

### **Recommended Operating Range:**

(TA=+25℃, unless otherwise specified)

| Parameter               | Symbol | MIN. | TYP. | MAX. | Unit |
|-------------------------|--------|------|------|------|------|
| Drain to Source Voltage | VDS    | +1   | +2   | +3   | V    |
| Drain Current           | ID     | 5    | 10   | 15   | mA   |

#### **Electrical Characteristics:**

(TA=+25℃, unless otherwise specified)

| Parameter                      | Symbol   | Condition        | MIN.  | TYP.  | MAX.  | Unit |
|--------------------------------|----------|------------------|-------|-------|-------|------|
| Gate to Source Leak Current    | IGSO     | VGS=-3.0V        | -     | 0.4   | 10    | μΑ   |
| Saturated Drain Current        | IDSS     | VDS=2V, VGS=0V   | 27    | 47.5  | 68    | mA   |
| Gate to Source Cut-off Voltage | VGS(off) | VDS=2V, ID=120μA | -1.10 | -0.75 | -0.39 | V    |
| Transconductance               | Gm       | VDS=2V, ID=10mA  | 54    | 69    | -     | mS   |
| Noise Figure                   | NF       | VDS=2V, ID=10mA, | -     | 0.30  | 0.50  | dB   |
| Associated Gain                | Ga       | f=12GHz          | 12.5  | 13.7  | -     | dB   |

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150



#### **Typical Characteristics:**

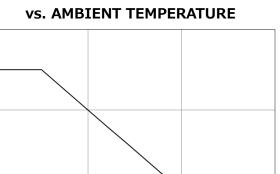
150

Total Power Dissipation P<sub>ToT</sub> (mW)

0 0

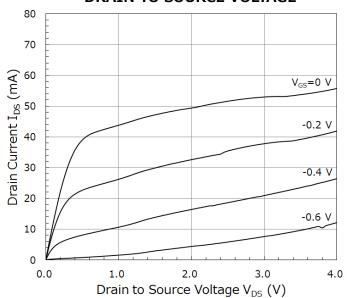
(TA=+25℃, unless otherwise specified)

**TOTAL POWER DISSIPATION** 

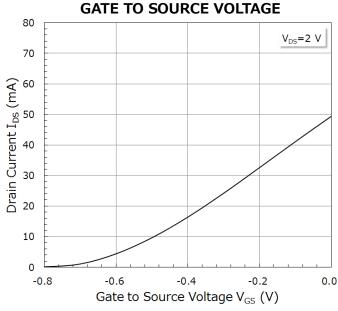


Ambient Temperature  $T_A$  (deg C)

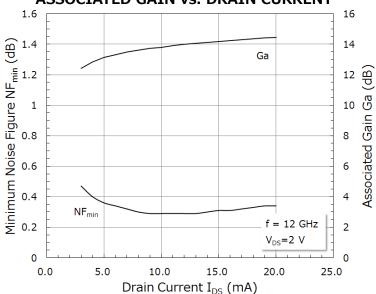
## **DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE**







### **MINIMUM NOISE FIGURE & ASSOCIATED GAIN vs. DRAIN CURRENT**



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#### S-Parameters:

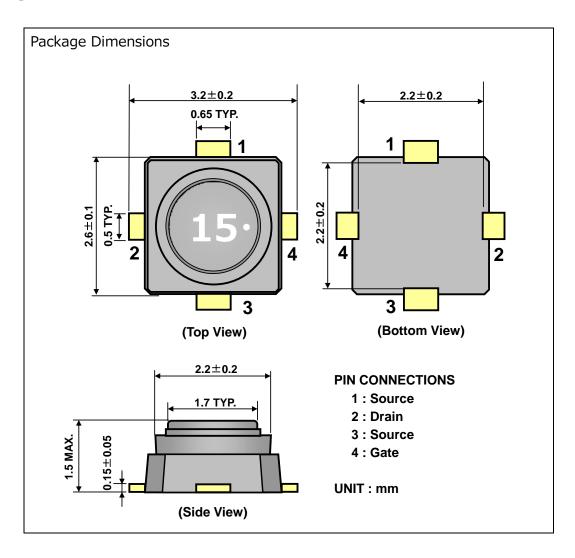
S-parameters/Noise parameters are provided on the CDK Web site. [Original Products]  $\rightarrow$  [Low Noise GaAsFET for LNB]  $\rightarrow$  [Device Parameters] URL http://www.en.cdk.co.jp/products/highfrequency/rf/LNGaAsFET/LNB/index.html

#### **RF Measuring Layout Pattern:**

RF Measuring Layout Patterns are provided on the CDK Web site.  $[\mbox{Original Products}] \rightarrow [\mbox{Low Noise GaAsFET for LNB}] \rightarrow [\mbox{Design Support}] \rightarrow [\mbox{Evaluation Board Information}]$ 

URL <a href="http://www.en.cdk.co.jp/products/highfrequency/rf/LNGaAsFET/LNB/designsupport/index.html">http://www.en.cdk.co.jp/products/highfrequency/rf/LNGaAsFET/LNB/designsupport/index.html</a>

#### **Package Dimensions:**



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### **Recommended Soldering Conditions:**

Recommended Soldering Conditions are provided on the CDK Web site.  $[Original\ Products] \rightarrow [Low\ Noise\ GaAsFET\ for\ LNB] \rightarrow [Design\ Support] \rightarrow [others] \\ URL\ \underline{http://www.en.cdk.co.jp/products/highfrequency/rf/LNGaAsFET/LNB/designsupport/index.html}$ 

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[Caution in the gallium arsenide (GaAs) product handling]

This product uses gallium arsenide (GaAs) of the toxic substance appointed in laws and ordinances. GaAs vapor and powder are hazardous to human health if inhaled or ingested.

- Do not dispose in fire or break up this product.
- Do not chemically make gas or powder with this product.
- When discard this product, please obey the law of your country.
- Do not lick the product or in any way allow it to enter the mouth.

#### [CAUTION]

Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

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